

**In the Claims:**

Please amend claim 6 as indicated below. This listing of claims replaces all prior versions.

1. (Previously presented) A power amplifier comprising:
  - a first stage including a signal amplification transistor for amplifying an input signal, the signal amplification transistor having a control electrode responsive to a bias current, and
  - a first bias circuit including a controlled current source for converting a gain control voltage to the bias current.
2. (Previously presented) The power amplifier according to claim 1, wherein the first bias circuit includes a non-linear voltage/current converter for converting the gain control voltage to the bias current, the non-linear voltage/current converter coupled to a current mirror for providing the bias current to the control electrode.
3. (Previously presented) The power amplifier according to claim 2, wherein the non-linear voltage/current converter includes at least one differential stage coupled to a reference voltage.
4. (Previously presented) A power amplifier comprising:
  - a first stage for amplifying an input signal, and
  - a first bias circuit for providing a bias current to the first stage,
  - the first bias circuit including a controlled current source, and the first bias circuit being arranged for feeding its bias current to a control electrode of a signal amplification transistor of the first stage, wherein at least one bias circuit comprises two distinct voltage/current converters for converting two distinct gain control voltages.
5. (Previously presented) The power amplifier according to claim 1, wherein the first bias circuit further includes bias voltage means for providing a bias voltage to the first stage.

6. (Currently amended) A power amplifier comprising:
  - a first stage for amplifying an input signal, and
  - a first bias circuit for providing a bias current to the first stage,
  - the first bias circuit including a controlled current source and a voltage/current converter, and the first bias circuit being arranged for feeding its bias current to a control electrode of a signal amplification transistor of the first stage, wherein in the first bias circuit an additional transistor is coupled between the voltage/current converter and the controlled current source so as to compensate for ~~the~~ a DC current gain of the signal amplification transistor.
7. (Previously presented) The power amplifier according claim 1, further comprising:
  - a second stage for amplifying a signal output by the first stage;
  - a second bias circuit for providing a second stage bias current to the second stage;
  - a third stage for amplifying a signal output by the second stage; and
  - third bias circuit for providing a third stage bias current to the third stage.
8. (Previously presented) The power amplifier according to claim 1, wherein the power amplifier is arranged for amplifying high frequency signals.
9. (Original) A device provided with a power amplifier according to claim 1.